



Aurora's Technological and Research Institute

Parvathapur, Uppal, Hyderabad – 500 039

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASS TIME TABLE– II SEM 2017-2018

ECE-IA

ROOM NO.: 214B

	9.00 10.00	10.00 11.00	11.00 12.00	12.00 1.00	1.00 1.30	1.30 2.30	2.30 3.30	3.30 4.30
MON	LIB	EP(B1)/EC(B2/CP(B3) LAB			L U N C H B R E A K	M-II	EP	M-III
TUE	M-II	EP	M-III			EP(B2)/EC(B3/CP(B1) LAB		
WED	EP	CP		M-II		EG		
THU	M-III	EG				M-II		CP
FRI	CP	M-II	EP	M-III		EP(B3)/EC(B1/CP(B2) LAB		
SAT	M-III		CP			EP		SPORTS

	Subject	Faculty Name
MII	Mathematics –II	Ms.M.Swathi
MIII	Mathematics-III	Ms.L.Eswaramma
CP	Computer Programming in C	Ms.K.Kavitha
EP	Engineering Physics	Mr.Purnachander Rao
EG	Engineering Graphics Practice	Mr.Kartheek/Mr.Narsimha Goud/Mr.G.Umesh
EP LAB	Engineering Physics Laboratory	Mr.Purnachander Rao
EC LAB	Engineering Chemistry Laboratory	Dr.K.NagaJyothi/Ms.Hima Bindu
CP LAB	Computer Programming in C laboratory	Ms.K.Kavitha

LAB BATCHES		LAB ROOM NO.	
17841A0401-423	B1	EP LAB	306 (MB)
17841A0424-445	B2	EC LAB	307A(MB)
17841A0446-467	B3	CP LAB	214A(MB)

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASS TIME TABLE– II SEM 2017-2018

ECE-IB

ROOM NO.: 214C

	9.00 10.00	10.00 11.00	11.00 12.00	12.00 1.00	1.00 1.30	1.30 2.30	2.30 3.30	3.30 4.30
MON	M-III	EG			L U N C H B R E A K	EP		CP
TUE	M-II	CP	M-III	EP		EG		
WED	M-III	EP(B1)/EC(B2/CP(B3) LAB				M-II		EP
THU	CP	M-III	M-II			EP(B2)/EC(B3/CP(B1) LAB		
FRI	EP	EP(B3)/EC(B1/CP(B2) LAB				M-III		CP
SAT	M-III		CP			M-II		SPORTS

	Subject	Faculty Name
MII	Mathematics –II	Ms.D.Saritha
MIII	Mathematics-III	Ms.D.Kalyani
EP	Engineering Physics	Dr.Ajay
CP	Computer Programming in C	Ms.G.Varalakshmi
EG	Engineering Graphics Practice	Mr.Swamy/Mr.Gangadhar/Mr.Anurag
EP LAB	Engineering Physics Laboratory	Mr.Ajay Kumar
EC LAB	Engineering Chemistry Laboratory	Mr.D.Ugandhar/Ms.B.Hima Bindu
CP LAB	Computer Programming in C laboratory	Ms.G.Varalakshmi

LAB BATCHES		LAB ROOM NO.	
17841A0468 – 490	B1	EP LAB	306 (MB)
17841A0491 – 4B2	B2	EC LAB	307A(MB)
17841A04B3 – 4D5	B3	CP LAB	214A(MB)

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASS TIME TABLE– II SEM 2017-2018

ECE II A

Room no-208A

	9.00 10.00	10.00 11.00	11.00 12.00	12.00 1.00	1.00 1.30	1.30 2.30	2.30 3.30	3.30 4.30
MON	AC	GS		LIB	L U N C H B R E A K	AE(B1)/AC(B2)/PDC(B3)		
TUE	PDC	CS		STLD		AC		SPORTS
WED	BEFA	AE(B2)/AC(B3)/PDC(B1)				STLD	CS	PDC
THU	CS	PDC	BEFA			AE(B3)/AC(B1)/PDC(B2)		
FRI	CS	AC	PDC			BEFA	STLD	
SAT	AC		STLD			Student Seminars		

	Subject	Faculty Name
STLD	Switching Theory and Logic Design	Ms.N.Nirmala Devi
PDC	Pulse and Digital Circuits	Mr.K.Satish Babu
CS	Control Systems	Ms.Laxmi Prasanna
AC	Analog Communications	Ms.J.Santhoshini
BEFA	Business Economics and Financial Analysis	Ms.Mary Francina
AC Lab	Analog Communications Lab	Ms.J.Santhoshini
PDC Lab	Pulse and Digital Circuits Lab	Mr.K.Satish Babu & Ms.Komal Kumari, & Ms. Pushpalatha
AE Lab	Analog Electronics Lab	Ms.N.Nirmala Devi & Mr.Mahender reddy & Ms. Sunanda Priya darshini
GS Lab	Gender Sensitization Lab	Dr.Poonam Minocha

Lab Batches		Lab Room No.
Batch1	16841A0401-418	AE Lab – 101/215
Batch2	16841A0419 – 435	AC Lab – 214D
Batch3	16841A0436 - 453	PDC Lab – 201B

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Aurora's Technological and Research Institute

Parvathapur, Uppal, Hyderabad – 500 039

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASS TIME TABLE– II SEM 2017-2018

ECE II B

Room no-210

	9.00 10.00	10.00 11.00	11.00 12.00	12.00 1.00	1.00 1.30	1.30 2.30	2.30 3.30	3.30 4.30
MON	LIB	AE(B1)/AC(B2)/PDC(B3)			L U N C H B R E A K	PDC	STLD	BEFA
TUE	BEFA	AC		CS		STLD		PDC
WED	PDC	GS		AC		AE(B2)/AC(B3)/PDC(B1)		
THU	BEFA	CS	PDC			AC		SPORTS
FRI	STLD	AE(B3)/AC(B1)/PDC(B2)				CS		BEFA
SAT	STLD		CS			Student Seminars		

	Subject	Faculty Name
STLD	Switching Theory and Logic Design	Ms.Mamatha .J
PDC	Pulse and Digital Circuits	Ms.K.Thrisandhya
CS	Control Systems	Ms.Prasanna laxmi
AC	Analog Communications	Ms.N.swathi
BEFA	Business Economics and Financial Analysis	Ms.Mary Francina
AC Lab	Analog Communications Lab	Ms.N.swathi
PDC Lab	Pulse and Digital Circuits Lab	Ms.K.Thrisandhya & Ms.K.Shirisha & Mr. Ashok R
AE Lab	Analog Electronics Lab	Dr.Amith Gangopadyay & Ms.T.Sirisha & M. Laxmi
GS Lab	Gender Sensitization Lab	Mr.Jaya Srinivas Rao

Lab Batches		Lab Room No.
Batch1	16841A0454 – 471	AE Lab – 101/215
Batch2	16841A0472 – 488	AC Lab – 214D
Batch3	16841A0489 – 4A4	PDC Lab – 201B

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Aurora's Technological and Research Institute

Parvathapur, Uppal, Hyderabad – 500 039

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASS TIME TABLE– II SEM 2017-2018

ECE II C

Room no-208

	9.00 10.00	10.00 11.00	11.00 12.00	12.00 1.00	1.00 1.30	1.30 2.30	2.30 3.30	3.30 4.30
MON	BEFA	PDC	CS	STLD	L U N C H B R E A K	AC	GS	
TUE	LIB	AE(B1)/AC(B2)/PDC(B3)				AC	STLD	BEFA
WED	STLD	AC	BEFA			CS	PDC	SPORT S
THU	CS	AE(B2)/AC(B3)/PDC(B1)				PDC	STLD	
FRI	AC	PDC		CS		AE(B3)/AC(B1)/PDC(B2)		
SAT	CS		AC			Student Seminars		

	Subject	Faculty Name
STLD	Switching Theory and Logic Design	Ms.T.Jyothsna
PDC	Pulse and Digital Circuits	Ms.P.Mamatha
CS	Control Systems	Mr.Rajender
AC	Analog Communications	Ms.Shilpa.V
BEFA	Business Economics and Financial Analysis	Ms.Mary Francina
AC Lab	Analog Communications Lab	Ms.Shilpa.V
PDC Lab	Pulse and Digital Circuits Lab	Ms.P.Mamatha & Mr.Mahender reddy & Ms. A. Akshitha
AE Lab	Analog Electronics Lab	Ms.K.Aparna & Ms.J.Mamatha & Mr. G. Rajesh
GS Lab	Gender Sensitization Lab	Dr.Poonam Minocha

Lab Batches		Lab Room No.
Batch1	16841A04A5 – 4C2	AE Lab – 101/215
Batch2	16841A04C3 – 4D9	AC Lab – 214D
Batch3	16841A04E0 – 4F3, 15841A0420-422 17845A0401, 402 16621A0487	PDC Lab – 201B

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Aurora's Technological and Research Institute

Parvathapur, Uppal, Hyderabad – 500 039

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASS TIME TABLE– II SEM 2017-2018

ECE III A

Room no-213

	9.00 10.00	10.00 11.00	11.00 12.00	12.00 1.00	1.00 1.30	1.30 2.30	2.30 3.30	3.30 4.30
MON	HVPE	DSP	MEFA		L U N C H B R E A K	MPMC LAB(B1)/ DSP LAB(B2)		
TUE	VLSI	DSP		HVPE		MPMC	DC	
WED	DC	VLSI	MEFA	MPMC		HVPE		SPORTS
THU	LIB	MPMC LAB(B2)/ DSP LAB(B1)				DC	VLSI	LIB
FRI	DSP	MPMC		DC		MEFA	VLSI	
SAT	DSP		MPMC			Student Seminars		

	Subject	Faculty Name
MEFA	Managerial Economics and Financial analysis	Ms. K. Swathi
HVPE	Human Values and Professional Ethics	Ms.Mary Francina
DC	Digital Communications	Mr.G.Anand Babu
VLSI	VLSI Design	Ms.K.Thrisandhya
MPMC	Microprocessors and Microcontrollers	Mr.Md.Nizamuddin salman
DSP	Digital Signal Processing	Ms.T.Jyothsna
MPMC LAB	Microprocessors and Microcontrollers Lab	Mr.Md.Nizamuddin salman & Mr. B. Srinivas & Ms. U. Mounika
DSP LAB	Digital Signal Processing Lab	Ms.T.Jyothsna, Mr. T. Jayaramulu

Lab Batches		Lab Room No.
Batch1	15841A0401 – 432	MPMC Lab – 203A
Batch2	15841A0433 – 444 14841A0490, 434, 492, 4A3, 4B1, 4C9, 4E0, 4F1, 4G1, 4G3, 4H1	DSP Lab – 214A

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Aurora's Technological and Research Institute

Parvathapur, Uppal, Hyderabad – 500 039

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASS TIME TABLE– II SEM 2017-2018

ECE III B

Room no-216

	9.00 10.00	10.00 11.00	11.00 12.00	12.00 1.00	1.00 1.30	1.30 2.30	2.30 3.30	3.30 4.30
MON	MPMC	DSP	HVPE		L U N C H B R E A K	DC	VLSI	SPORTS
TUE	LIB	MPMC LAB(B1)/ DSP LAB(B2)				MEFA	VLSI	DC
WED	DC	VLSI	MPMC	DSP		MPMC LAB(B2)/ DSP LAB(B1)		
THU	DC	DSP		MEFA		VLSI	MPMC	HVPE
FRI	DSP	MEFA		HVPE		MPMC		LIB
SAT	VLSI		DC			Student Seminars		

	Subject	Faculty Name
MEFA	Managerial Economics and Financial analysis	Ms. K. Swathi
HVPE	Human Values and Professional Ethics	Ms.Mary Francina
DC	Digital Communications	Ms.N.Swathi
VLSI	VLSI Design	Dr.Amith Gangopadyay
MPMC	Microprocessors and Microcontrollers	Ms.K.Supriya
DSP	Digital Signal Processing	Mr.G.Anand Babu
MPMC LAB	Microprocessors and Microcontrollers Lab	Ms.K.Supriya, & Mr. Sampath kumar
DSP LAB	Digital Signal Processing Lab	Mr.G.Anand Babu, Mr. L. Srinivas

Lab Batches		Lab Room No.
Batch1	15841A0445 - 464	MPMC Lab – 203A
Batch2	15841A0465 – 482, 16845A0401, 402	DSP Lab – 214A

HOD

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Aurora's Technological and Research Institute

Parvathapur, Uppal, Hyderabad – 500 039

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASS TIME TABLE– II SEM 2017-2018

ECE IV A

Room no-113

	9.00 10.00	10.00 11.00	11.00 12.00	12.00 1.00	1.00 1.30	1.30 2.30	2.30 3.30	3.30 4.30	
MON	WCN		RS		L U N C H B R E A K	SC		LIB	
TUE	WCN		RS			SC		Sports	
WED	Project Work					Project Work			
THU	Project Work					Project Work			
FRI	Project Work					Project Work			
SAT	Project Work					Project Work			

	Subject	Faculty Name
SC	Satellite Communications	Ms.P.Mamatha
RS	Radar Systems	Mr. M. Sravan Kumar Reddy
WCN	Wireless Communications and Networks	Mr.Md.Nizamuddin Salman

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASS TIME TABLE– II SEM 2017-2018

ECE IVB

Room no-115

	9.00 10.00	10.00 11.00	11.00 12.00	12.00 1.00	1.00 1.30	1.30 2.30	2.30 3.30	3.30 4.30	
MON	RS		WCN		L U N C H B R E A K	SC			
TUE	WCN		SC			RS			
WED	Project Work					Project Work			
THU	Project Work					Project Work			
FRI	Project Work					Project Work			
SAT	Project Work					Project Work			

	Subject	Faculty Name
SC	Satellite Communications	Mr.Vinod Chavan
RS	Radar Systems	Mr.K.Satish Babu
WCN	Wireless Communications and Networks	Ms.Komal Kumari

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASS TIME TABLE– II SEM 2017-2018

ECE IVC

Room no-301

	9.00 10.00	10.00 11.00	11.00 12.00	12.00 1.00	1.00 1.30	1.30 2.30	2.30 3.30	3.30 4.30	
MON	SC		RS		L U N C H B R E A K	WCN		LIB	
TUE	WCN		SC			RS		LIB	
WED	Project Work					Project Work			
THU	Project Work					Project Work			
FRI	Project Work					Project Work			
SAT	Project Work					Project Work			

	Subject	Faculty Name
SC	Satellite Communications	Mr.K.Harikrishna
RS	Radar Systems	Ms.K.supriya
WCN	Wireless Communications and Networks	Ms.T.Sirisha

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